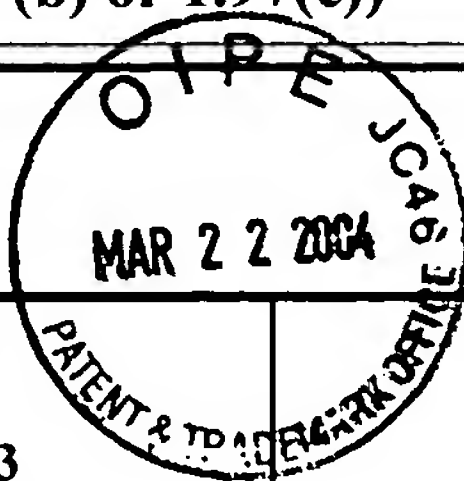


**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.  
**BUR920030061US1**

In Re Application Of: **Bergman Reuter et al.**



Serial No.  
**10/605,849**

Filing Date  
**10/30/2003**

Examiner  
**Unknown**

Group Art Unit  
**Unknown**

Title: **System for Search and Analysis of Systematic Defects in Integrated Circuits**

Address to:  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**37 CFR 1.97(b)**

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

**37 CFR 1.97(c)**

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

**OR**

☐ the fee set forth in 37 CFR 1.17(p).

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
(Under 37 CFR 1.97(b) or 1.97(e))

Docket No.  
**BUR920030061US1**

In Re Application: **Bergman Reuter et al.**

**MAR 22 2004**

Serial No.

**10/605,849**

Filing Date

**10/30/2003**

Examiner

**Unknown**

Group Art Unit

**Unknown**

**System for Search and Analysis of Systematic Defects in Integrated Circuits**

**Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of \_\_\_\_\_ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. **09-0456**  
as described below.
- ☐ Charge the amount of \_\_\_\_\_
- ☒ Credit any overpayment.
- ☒ Charge any additional fee required.

**Certificate of Transmission by Facsimile\***

I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (F:

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**Certificate of Mailing by First Class Mail**

I certify that this document and fee is being deposited on **03/17/04** with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

\_\_\_\_\_  
Signature of Person Mailing Correspondence

**C. MUELLER**  
Typed or Printed Name of Person Mailing Certificate

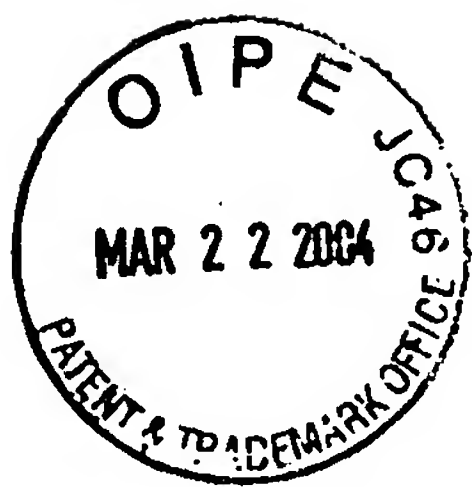
**\*This certificate may only be used if paying by deposit account.**

**Richard M. Kotulak**  
Signature

Dated: **3/18/04**

**Richard M. Kotulak, Reg. No. 27,712**  
**IP Law Department, 972E**  
**IBM Corporation**  
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**Essex Junction, VT 05452**  
**Telephone: 802-769-4457**

CC:



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Bergman Reuter et al.

Serial No.: <sup>10/605849</sup> ~~Not Yet Assigned~~

Group Art Unit: Unknown

Filing Date: <sup>10/30/2003</sup> ~~Concurrently Herewith~~

Examiner: Unknown

For: SYSTEM FOR SEARCH AND ANALYSIS OF SYSTEMATIC DEFECTS IN  
INTEGRATED CIRCUITS

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicants' duty of disclosure under 37 CFR §1.56, applicants respectfully bring the following documents, listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner. This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicants are aware.

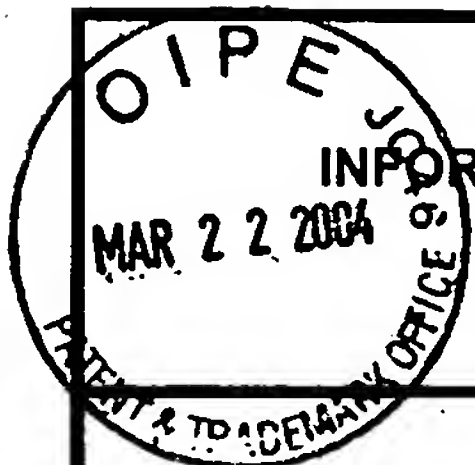
It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 09-0456.

Respectfully submitted,

Frederick W. Gibb, III  
Registration No. 37,629

McGinn & Gibb, PLLC  
2568-A Riva Road, Suite 304  
Annapolis, Maryland 21401  
(410) 573-1545  
Customer No. 29154



INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

ATTY DOCKET NO.  
BUR920030061US1

SERIAL NO. 10/605,849  
~~NOT YET ASSIGNED~~

FILING 10/30/2003

GROUP  
Unknown

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	JP10301257	11/13/98	Japan				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		Friedman et al., "Model-Free Estimation of Defect Clustering in Integrated Circuit Fabrication", IEEE Transactions on Semiconductor Manufacturing", Vol. 10, No. 3, August, 1997, pp. 344-359.

EXAMINER	DATE CONSIDERED
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.